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Automatic Abstraction for Intervals using Boolean Formulae

Jörg Brauer¹ and Andy King²

¹ Embedded Software Laboratory, RWTH Aachen University, Germany

² Portcullis Computer Security, Pinner, UK

Abstract. Traditionally, transfer functions have been manually designed for each operation in a program. Recently, however, there has been growing interest in computing transfer functions, motivated by the desire to reason about sequences of operations that constitute basic blocks. This paper focuses on deriving transfer functions for intervals — possibly the most widely used numeric domain — and shows how they can be computed from Boolean formulae which are derived through bit-blasting. This approach is entirely automatic, avoids complicated elimination algorithms, and provides a systematic way of handling wrap-arounds (integer overflows and underflows) which arise in machine arithmetic.

1 Introduction

The key idea in abstract interpretation [6] is to simulate the execution of each concrete operation $g : C \rightarrow C$ in a program with an abstract analogue $f : D \rightarrow D$ where C and D are domains of concrete values and descriptions. Each abstract operation f is designed to faithfully model its concrete counterpart g in the sense that if $d \in D$ describes a concrete value $c \in C$, sometimes written relationally as $d \propto c$ [17], then the result of applying g to c is described by the action of applying f to d , that is, $f(d) \propto g(c)$. Even for a fixed set of abstractions, there are typically many ways of designing the abstract operations. Ideally the abstract operations should compute abstractions that are as descriptive, that is, as accurate as possible, though there is usually interplay with accuracy and complexity, which is one reason why the literature is so rich. Normally the abstract operations are manually designed up front, prior to the analysis itself, but there are distinct advantages in synthesising the abstract operations from their concrete versions as part of the analysis itself, in a fully automatic way.

1.1 The drive for automatic abstraction

One reason for automation stems from operations that arise in sequences that are known as blocks. Suppose that such a sequence is formed of n concrete operations g_1, g_2, \dots, g_n , and each operation g_i has its own abstract counterpart f_i , henceforth referred to as its transfer function. Suppose too that the input to the sequence $c \in C$ is described by an input abstraction $d \in D$, that is, $d \propto c$.

Then the result of applying the n concrete operations to the input (one after another) is described by applying the composition of the n transfer functions to the abstract input, that is, $f_n(\dots f_2(f_1(d))) \times g_n(\dots g_2(g_1(c)))$. However, a more accurate result can be obtained by deriving a single transfer function f for the block $g_n \circ \dots \circ g_2 \circ g_1$ as a whole, designed so that $f(d) \times g_n(\dots g_2(g_1(c)))$. The value of this approach has been demonstrated for linear congruences [11] in the context of verifying bit-twiddling code [14]. Since blocks are program dependent, such an approach relies on automation rather than human intervention.

Another compelling reason for automation is the complexity of the concrete operations themselves. Even a simple concrete operation, such as increment by one, is complicated by the finite nature of computer arithmetic: if increment is applied to the largest integer that can be stored in a word, then the result is the smallest integer that is representable. As the transfer function needs to faithfully simulate concrete increment, then the corner case inevitably manifests itself (if not in the transfer function itself then elsewhere [29]). The problem of deriving transfer functions for low-level instructions, such as those of the x86, is particularly acute [2] since these operations not only update registers and memory locations, but also side effect status flags. Automatic abstraction offers a way to potentially tame this complexity.

1.2 Specifying extreme values with universal quantifiers

Monniaux [20] recently addressed the vexing question of automatic abstraction by focussing on template domains [28] which include, most notably, intervals [7]. He showed that if the concrete operations are specified as piecewise linear functions, then it is possible to derive transfer functions for blocks. The transfer functions relate the values of variables on entry to a block to their values on exit. To illustrate, suppose the variables x, y and z occur in a block and consider the maximum value of x on exit from the block. Monniaux shows how quantification can be used to specify the maximal output value of x in terms of the extreme values that x, y and z can take on entry to the block. The specification states that: the maximal output value of x is an upper bound on all the output values of x that are feasible for the values of x, y and z that fall within their input ranges. It also asserts that: the maximal output value of x is smaller than any other upper bound on the output value of x . These requirements are naturally formulated with universal quantification. Universal quantifier elimination is then used to find a direct linear relationship between the maximal value of x on exit and the ranges of x, y and z on entry; it is direct in that intermediate variables that occur in the specification are removed. This construction is ingenious but no polynomial elimination algorithm is known for piecewise systems, or is ever likely to exist [3]. Indeed, this computational bottleneck remains a problem [21].

1.3 Finessing universal quantifiers with Boolean formulae

This paper suggests that as an alternative to operating over piecewise linear systems one can instead express the semantics of a basic block with a Boolean

formula; an idea that is familiar in model checking where it is colloquially referred to as bit-blasting. Since Boolean formulae are more expressive than piecewise linear formulae, one would expect universal quantifier elimination to be just as difficult for Boolean formulae (or even harder since they are discrete). However, this is not so. To illustrate, consider $\forall x.f$ where $f = (x \vee \neg y) \wedge (\neg x \vee y \vee \neg z)$. Then $\forall x.f = f[x \mapsto 0] \wedge f[x \mapsto 1] = (\neg y) \wedge (y \vee \neg z)$. Observe that $\forall x.f$ can be obtained directly from f by removing the x and $\neg x$ literals from all the clauses of f . This is no coincidence and holds for any formula f presented in CNF not containing a vacuous clause that includes both x and $\neg x$ [15]. This suggests the following four step method for automatically deriving transfer functions for intervals (and related domains [18, 19]): First, use bit-vector logic to represent the semantics of a block as a single CNF formula f_{block} (an excellent tutorial on flattening bit-vector logic into propositional logic is given in [15, Chap. 6]). Thus each n -bit integer variable is represented as a separate vector of n propositional variables. Second, apply the specification of Monniaux [20, Sect. 3.2] to express the maximal value (or conversely the minimal value) of an output bit-vector in terms of the ranges on the input bit-vectors. This gives a propositional formula f_{spec} which is essentially f_{block} augmented with universal quantifiers. Third, the universal quantifiers are removed from f_{spec} to obtain f_{simp} – a simplification of f_{spec} . Thus although universal qualification is a hinderance for linear piecewise functions, it actually helps in a propositional formulation. Of course, f_{simp} is just a formula and does not prescribe how to compute a transfer function. However, a transfer function can be extracted from f_{simp} by abstracting f_{simp} with linear affine equations [13] which directly relate the output ranges to the input ranges. This fourth step (which is analogous to that proposed for abstracting formulae with congruences [14]) is the final step in the construction. Overall, this new approach to computing transfer functions confers the following advantages:

- it is amenable to instructions whose semantics is presented as Boolean formulae. The force of this is that propositional encodings are readily available for instructions, due to the rise in popularity of SAT-based model checking. Moreover, it is not obvious how to express the semantics of some (bit-level) instructions with piecewise linear functions;
- it avoids the computational problems associated with eliminating variables from piecewise linear systems;
- it distills transfer functions from Boolean formulae that are action systems of guarded updates. The guards are systems of octagonal constraints [19]. A guard tests whether a particular behaviour can arise, for example, whether an operation wraps, and the update revises the ranges accordingly. One guarded update might be applicable when an operation underflows and another when it overflows, thus a transfer function is a system of guarded updates. The updates are expressed with affine equations that specify how the extreme values of variables at the end of the block relate to their extreme values on entry into the block. The guards that are derived are optimal (for the class of octagons) as are the update operations (for the class of affine equalities). Once derived, a transfer function is evaluated using linear programming.

2 Worked Examples

The ethos of our approach is to express the semantics of a block in the computational domain of Boolean formulae. This concrete domain is rich enough to allow the extreme values (ranges) of variables to be specified in a way that is analogous to that of Monniaux [20]. However, in contrast to Monniaux, universal quantifier elimination is performed in the concrete setting, which is attractive computationally. Abstraction is then applied to synthesise guarded updates from quantifier-free formulae. Thus, the approach of Monniaux is abstraction then elimination, whereas ours is elimination then abstraction. We illustrate the power of this transposition by deriving transfer functions for some illustrative blocks of ATmega16 8-bit microcontroller instructions [1].

2.1 Deriving a transfer function for a block

Consider deriving a transfer function for the sequence of instructions `EOR R0 R1; EOR R1 R0; EOR R0 R1` that constitutes a block. An instruction `EOR R0 R1` stores the exclusive-or of registers `R0` and `R1` in `R0`. The operands are unsigned. To specify the semantics of the block, let $\mathbf{r0}$ and $\mathbf{r1}$ denote 8-bit vectors of propositional variables that will be used to represent the symbolic initial values of `R0` and `R1`, and likewise let $\mathbf{r0}'$ and $\mathbf{r1}'$ be bit-vectors of propositional variables that denote their final values. Furthermore, let $\mathbf{x}[i]$ denote the i^{th} element of the bit-vector \mathbf{x} where $\mathbf{x}[0]$ is the low bit. By introducing a bit-vector \mathbf{y} to denote the intermediate value of `R0` (which is akin to applying static single assignment) the semantics of the block can be stated propositionally as:

$$\varphi(\mathbf{y}) = (\bigwedge_{i=0}^7 \mathbf{y}[i] \leftrightarrow \mathbf{r0}[i] \oplus \mathbf{r1}[i]) \wedge (\bigwedge_{i=0}^7 \mathbf{r1}'[i] \leftrightarrow \mathbf{y}[i] \oplus \mathbf{r1}[i]) \wedge (\bigwedge_{i=0}^7 \mathbf{r0}'[i] \leftrightarrow \mathbf{y}[i] \oplus \mathbf{r1}'[i])$$

where \oplus denotes exclusive-or. Such formulae can be derived algorithmically by composing formulae [5, 14] – one formula for each instruction in the sequence.

The formula $\varphi(\mathbf{y})$ specifies the relationship between the inputs $\mathbf{r0}$ and $\mathbf{r1}$ and the outputs $\mathbf{r0}'$ and $\mathbf{r1}'$, but not a relationship between their ranges. This has to be derived. To do so, let the bit-vectors $\mathbf{r0}_\ell$ and $\mathbf{r0}_u$ (resp. $\mathbf{r1}_\ell$ and $\mathbf{r1}_u$) denote the minimal and maximal values for $\mathbf{r0}$ (resp. $\mathbf{r1}$). To express these ranges in propositional logic, define the formula:

$$\mathbf{x} \leq \mathbf{y} = (\mathbf{x}[7] \wedge \neg \mathbf{y}[7]) \vee (\bigvee_{j=0}^6 (\neg \mathbf{x}[j] \wedge \mathbf{y}[j] \wedge (\bigwedge_{k=j+1}^7 \mathbf{x}[k] \leftrightarrow \mathbf{y}[k])))$$

and for abbreviation let $\mathbf{x} \leq \mathbf{y} \leq \mathbf{z} = (\mathbf{x} \leq \mathbf{y}) \wedge (\mathbf{y} \leq \mathbf{z})$. Moreover, let $\phi = (\mathbf{r0}_\ell \leq \mathbf{r0} \leq \mathbf{r0}_u) \wedge (\mathbf{r1}_\ell \leq \mathbf{r1} \leq \mathbf{r1}_u)$ to express the requirement that $\mathbf{r0}$ and $\mathbf{r1}$ are confined to their ranges. With $\mathbf{r0}$ and $\mathbf{r1}$ in range, let $\mathbf{r0}_\ell^*$ and $\mathbf{r0}_u^*$ denote the resulting extreme values for $\mathbf{r0}'$. This amounts to requiring that, firstly, $\mathbf{r0}_\ell^*$ and $\mathbf{r0}_u^*$ are respectively lower and upper bounds on the range of $\mathbf{r0}'$. Secondly, any other lower and upper bounds on $\mathbf{r0}'$, say, $\mathbf{r0}'_\ell$ and $\mathbf{r0}'_u$, are respectively less or equal to and greater or equal to $\mathbf{r0}_\ell^*$ and $\mathbf{r0}_u^*$. Analogous

requirements hold for the extreme values $\mathbf{r1}_\ell^*$ and $\mathbf{r1}_u^*$ of $\mathbf{r1}'$. We impose the first requirement with the formula $\theta(\mathbf{y}) = \forall \mathbf{r0} : \forall \mathbf{r1} : \forall \mathbf{r0}' : \forall \mathbf{r1}' : \theta'(\mathbf{y})$ where:

$$\theta'(\mathbf{y}) = (\phi \wedge \varphi(\mathbf{y})) \Rightarrow (\mathbf{r0}_\ell^* \leq \mathbf{r0}' \leq \mathbf{r0}_u^* \wedge \mathbf{r1}_\ell^* \leq \mathbf{r1}' \leq \mathbf{r1}_u^*)$$

A quantifier-free version of $\theta(\mathbf{y})$ is then obtained by putting $\theta'(\mathbf{y})$ into CNF using standard transformations [25]. This introduces fresh variables, denoted \mathbf{y}' , and thus we write the CNF formula as $\theta''(\mathbf{y}, \mathbf{y}')$. The intermediate variables of \mathbf{y} and \mathbf{y}' (which are existentially quantified) are then removed by repeatedly applying resolution [15]. Those literals that involve variables in $\mathbf{r0}, \mathbf{r1}, \mathbf{r0}'$ and $\mathbf{r1}'$ are then simply struck out to obtain the desired quantifier-free model $\theta(\mathbf{y}, \mathbf{y}')$.

The second requirement is enforced by introducing other lower and upper bounds on $\mathbf{r0}'$ and $\mathbf{r1}'$, namely, $\mathbf{r0}'_\ell$ and $\mathbf{r0}'_u$, and $\mathbf{r1}'_\ell$ and $\mathbf{r1}'_u$. The requirement is formally stipulated as:

$$\psi(\mathbf{z}) = \forall \mathbf{r0}'_\ell : \forall \mathbf{r0}'_u : \forall \mathbf{r1}'_\ell : \forall \mathbf{r1}'_u : \forall \mathbf{r0} : \forall \mathbf{r1} : \forall \mathbf{r0}' : \forall \mathbf{r1}' : \psi'(\mathbf{z})$$

where:

$$\psi'(\mathbf{z}) = ((\phi \wedge \varphi(\mathbf{z})) \Rightarrow (\mathbf{r0}'_\ell \leq \mathbf{r0}' \leq \mathbf{r0}'_u \wedge \mathbf{r1}'_\ell \leq \mathbf{r1}' \leq \mathbf{r1}'_u)) \Rightarrow \kappa$$

and $\kappa = \mathbf{r0}'_\ell \leq \mathbf{r0}'_\ell \wedge \mathbf{r0}'_u \leq \mathbf{r0}'_u \wedge \mathbf{r1}'_\ell \leq \mathbf{r1}'_\ell \wedge \mathbf{r1}'_u \leq \mathbf{r1}'_u$. As before, we derive a quantifier-free version of $\psi(\mathbf{z})$, namely $\psi(\mathbf{z}, \mathbf{z}')$, where \mathbf{z}' are the fresh variables introduced in CNF conversion. To avoid accidental variable coupling between $\theta(\mathbf{y}, \mathbf{y}')$ and $\psi(\mathbf{z}, \mathbf{z}')$ we apply renaming (if necessary) to ensure that $(\text{var}(\mathbf{y}) \cup \text{var}(\mathbf{y}')) \cap (\text{var}(\mathbf{z}) \cup \text{var}(\mathbf{z}')) = \emptyset$ where $\text{var}(o)$ denotes the set of propositional variables in the object o .

Finally, the relationship between the bounds $\mathbf{r0}_\ell, \mathbf{r0}_u, \mathbf{r1}_\ell$ and $\mathbf{r1}_u$ and the extrema $\mathbf{r0}'_\ell, \mathbf{r0}'_u, \mathbf{r1}'_\ell$ and $\mathbf{r1}'_u$, is specified with the conjunction:

$$f_{\text{simp}} = \theta(\mathbf{y}, \mathbf{y}') \wedge \psi(\mathbf{z}, \mathbf{z}')$$

The formula f_{simp} is free from universal quantifiers and, moreover, we can abstract it using affine equations [13] to discover linear relationships between the variables of $S = \{\mathbf{r0}'_\ell, \mathbf{r0}'_u, \mathbf{r1}'_\ell, \mathbf{r1}'_u, \mathbf{r0}_\ell, \mathbf{r0}_u, \mathbf{r1}_\ell, \mathbf{r1}_u\}$ as desired. We regard this abstraction operation, denoted $\alpha_{\text{aff}}(f_{\text{simp}}, S)$, as a blackbox and defer presentation of the details to the following section. However, to state the outcome, let $\langle \mathbf{x} \rangle = \sum_{i=0}^7 2^i \mathbf{x}[i]$ where \mathbf{x} is an 8-bit vector of propositional variables. Then

$$\alpha_{\text{aff}}(f_{\text{simp}}, S) = \begin{cases} \langle \mathbf{r0}'_\ell \rangle = \langle \mathbf{r1}_\ell \rangle \wedge \langle \mathbf{r0}'_u \rangle = \langle \mathbf{r1}_u \rangle \wedge \\ \langle \mathbf{r1}'_\ell \rangle = \langle \mathbf{r0}_\ell \rangle \wedge \langle \mathbf{r1}'_u \rangle = \langle \mathbf{r0}_u \rangle \end{cases}$$

This shows that the ranges of R0 and R1 are swapped. Indeed, the sequence of EOR instructions is a common idiom for exchanging the contents of two registers without employing a third. The resulting transfer function can be realised with four updates. For this example, it is difficult to see how range analysis can be usefully performed without deriving a transfer function at this level of granularity. Furthermore, it is not clear how such a transfer function could be derived from a system of piecewise linear functions [20] since such systems cannot express exclusive-or constraints.

2.2 Deriving a transfer function for an operation with many modes

As a second example, consider computing a transfer function for the single operation `ADD R0 R1` which calculates the sum of `R0` and `R1` and stores the result in `R0`. We assume that the operands are signed and to interpret the value of such a vector let $\langle\langle \mathbf{x} \rangle\rangle = (\sum_{i=0}^6 2^i \mathbf{x}[i]) - 2^7 \mathbf{x}[7]$ where $\mathbf{x}[7]$ is read as the sign bit. The `ADD R0 R1` instruction is interesting because it is an exemplar of an instruction that can operate in one of three modes: it overflows (the sum exceeds 127); it underflows (the sum is strictly less than -128); or it neither overflows nor underflows (it is exact). The semantics in these respective modes, can be expressed with three Boolean formulae that are defined as follows:

$$\begin{aligned}\varphi_O(\mathbf{c}) &= \varphi(\mathbf{c}) \wedge (\neg \mathbf{r0}[7] \wedge \neg \mathbf{r1}[7] \wedge \mathbf{r0}'[7]) \\ \varphi_U(\mathbf{c}) &= \varphi(\mathbf{c}) \wedge (\mathbf{r0}[7] \wedge \mathbf{r1}[7] \wedge \neg \mathbf{r0}'[7]) \\ \varphi_E(\mathbf{c}) &= \varphi(\mathbf{c}) \wedge (\neg \mathbf{r0}[7] \vee \neg \mathbf{r1}[7] \vee \mathbf{r0}'[7]) \wedge (\mathbf{r0}[7] \vee \mathbf{r1}[7] \vee \neg \mathbf{r0}'[7])\end{aligned}$$

where \mathbf{c} is a bit-vector that represents the intermediate carry bits and:

$$\begin{aligned}\varphi(\mathbf{c}) &= \left(\bigwedge_{i=0}^7 \mathbf{r0}'[i] \leftrightarrow \mathbf{r0}[i] \oplus \mathbf{r1}[i] \oplus \mathbf{c}[i] \right) \wedge \\ &\quad \neg \mathbf{c}[0] \wedge \left(\bigwedge_{i=0}^6 \mathbf{c}[i+1] \leftrightarrow (\mathbf{r0}[i] \wedge \mathbf{r1}[i]) \vee (\mathbf{r0}[i] \wedge \mathbf{c}[i]) \vee (\mathbf{r1}[i] \wedge \mathbf{c}[i]) \right)\end{aligned}$$

It is important to appreciate that the formulae that model an instruction need to be prescribed just once for each instruction. Thus, the complexity is barely more than that of bit-blasting.

The transfer functions for multi-modal operations are formulated as action systems of guarded updates. Given a system of input intervals, which defines a hypercube, the guards test whether an update is applicable and, if so, the corresponding update is applied. The update maps the input intervals to the resulting output intervals. An update is applicable if interval and guard constraints are simultaneously satisfiable. The guards are formulated as octagonal constraints [19] over the inputs to the (trivial) block, namely, $S' = \{\mathbf{r0}, \mathbf{r1}\}$. Guards are derived with an abstraction operator, denoted $\alpha_{\text{oct}}(\varphi_i(\mathbf{c}), S')$, which discovers the octagonal inequalities that hold in the formula $\varphi_i(\mathbf{c})$ between the variables of S' . This abstraction can be calculated automatically, though for reasons of continuity we defer the details until the following section. Applying this abstraction to the three $\varphi_i(\mathbf{c})$ formulae yields the guards:

$$\begin{aligned}\alpha_{\text{oct}}(\varphi_O(\mathbf{c}), S') &= \begin{cases} 128 \leq \langle\langle \mathbf{r0} \rangle\rangle + \langle\langle \mathbf{r1} \rangle\rangle \leq 254 \wedge \\ 1 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq 127 & \wedge 1 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 127 \end{cases} \\ \alpha_{\text{oct}}(\varphi_U(\mathbf{c}), S') &= \begin{cases} -256 \leq \langle\langle \mathbf{r0} \rangle\rangle + \langle\langle \mathbf{r1} \rangle\rangle \leq -129 \wedge \\ -128 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq -1 & \wedge -128 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq -1 \end{cases} \\ \alpha_{\text{oct}}(\varphi_E(\mathbf{c}), S') &= \begin{cases} -128 \leq \langle\langle \mathbf{r0} \rangle\rangle + \langle\langle \mathbf{r1} \rangle\rangle \leq 127 \wedge \\ -128 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq 127 & \wedge -128 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 127 \end{cases}\end{aligned}$$

Guards are computed using perfect integers (the detail of which is explained in the following section) rather than modulo 256. Hence the linear inequality

$\langle\langle \mathbf{r0} \rangle\rangle + \langle\langle \mathbf{r1} \rangle\rangle \leq 254$ which follows from the positivity requirements on $\langle\langle \mathbf{r0} \rangle\rangle$ and $\langle\langle \mathbf{r1} \rangle\rangle$, namely, $1 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq 127$ and $1 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 127$.

An affine update is computed for each mode $\varphi_i(\mathbf{c})$ from the Boolean formula $f_{i,\text{simp}} = \theta_i(\mathbf{c}, \mathbf{c}') \wedge \psi_i(\mathbf{d}, \mathbf{d}')$ where $\theta_i(\mathbf{c}, \mathbf{c}')$ and $\psi_i(\mathbf{d}, \mathbf{d}')$ are quantifier-free formulae derived from $\varphi_i(\mathbf{c})$ in an analogous way to before. As before, we desire affine relationships over $S = \{\mathbf{r0}_\ell^*, \mathbf{r0}_u^*, \mathbf{r1}_\ell^*, \mathbf{r1}_u^*, \mathbf{r0}_\ell, \mathbf{r0}_u, \mathbf{r1}_\ell, \mathbf{r1}_u\}$, hence we calculate $\alpha_{\text{aff}}(f_{i,\text{simp}}, S)$ which yields:

$$\begin{aligned} \alpha_{\text{aff}}(f_{O,\text{simp}}, S) &= \begin{cases} \langle\langle \mathbf{r0}_\ell^* \rangle\rangle = \langle\langle \mathbf{r0}_\ell \rangle\rangle + \langle\langle \mathbf{r1}_\ell \rangle\rangle - 256 \wedge \\ \langle\langle \mathbf{r0}_u^* \rangle\rangle = \langle\langle \mathbf{r0}_u \rangle\rangle + \langle\langle \mathbf{r1}_u \rangle\rangle - 256 \wedge \\ \langle\langle \mathbf{r1}_\ell^* \rangle\rangle = \langle\langle \mathbf{r1}_\ell \rangle\rangle \wedge \langle\langle \mathbf{r1}_u^* \rangle\rangle = \langle\langle \mathbf{r1}_u \rangle\rangle \end{cases} \\ \alpha_{\text{aff}}(f_{U,\text{simp}}, S) &= \begin{cases} \langle\langle \mathbf{r0}_\ell^* \rangle\rangle = \langle\langle \mathbf{r0}_\ell \rangle\rangle + \langle\langle \mathbf{r1}_\ell \rangle\rangle + 256 \wedge \\ \langle\langle \mathbf{r0}_u^* \rangle\rangle = \langle\langle \mathbf{r0}_u \rangle\rangle + \langle\langle \mathbf{r1}_u \rangle\rangle + 256 \wedge \\ \langle\langle \mathbf{r1}_\ell^* \rangle\rangle = \langle\langle \mathbf{r1}_\ell \rangle\rangle \wedge \langle\langle \mathbf{r1}_u^* \rangle\rangle = \langle\langle \mathbf{r1}_u \rangle\rangle \end{cases} \\ \alpha_{\text{aff}}(f_{E,\text{simp}}, S) &= \begin{cases} \langle\langle \mathbf{r0}_\ell^* \rangle\rangle = \langle\langle \mathbf{r0}_\ell \rangle\rangle + \langle\langle \mathbf{r1}_\ell \rangle\rangle \wedge \\ \langle\langle \mathbf{r0}_u^* \rangle\rangle = \langle\langle \mathbf{r0}_u \rangle\rangle + \langle\langle \mathbf{r1}_u \rangle\rangle \wedge \\ \langle\langle \mathbf{r1}_\ell^* \rangle\rangle = \langle\langle \mathbf{r1}_\ell \rangle\rangle \wedge \langle\langle \mathbf{r1}_u^* \rangle\rangle = \langle\langle \mathbf{r1}_u \rangle\rangle \end{cases} \end{aligned}$$

When coupled with the guards, this gives an action system of three guarded updates reflecting the three distinct modes of operation.

To illustrate an application of the derived transfers function, suppose $\langle\langle \mathbf{r0} \rangle\rangle$ and $\langle\langle \mathbf{r1} \rangle\rangle$ are clamped to fall within given input range. Moreover, suppose the range is expressed as the following system of inequalities:

$$r = \begin{cases} \langle\langle \mathbf{r0}_\ell \rangle\rangle = -2 \wedge \langle\langle \mathbf{r0}_u \rangle\rangle = 5 & \wedge \langle\langle \mathbf{r0}_\ell \rangle\rangle \leq \langle\langle \mathbf{r0} \rangle\rangle \leq \langle\langle \mathbf{r0}_u \rangle\rangle \\ \langle\langle \mathbf{r1}_\ell \rangle\rangle = 1 & \wedge \langle\langle \mathbf{r1}_u \rangle\rangle = 126 \wedge \langle\langle \mathbf{r1}_\ell \rangle\rangle \leq \langle\langle \mathbf{r1} \rangle\rangle \leq \langle\langle \mathbf{r1}_u \rangle\rangle \end{cases}$$

In addition to bound the extreme output values let:

$$r' = \begin{cases} -128 \leq \langle\langle \mathbf{r0}_\ell^* \rangle\rangle \leq 127 \wedge -128 \leq \langle\langle \mathbf{r0}_u^* \rangle\rangle \leq 127 \wedge \\ -128 \leq \langle\langle \mathbf{r1}_\ell^* \rangle\rangle \leq 127 \wedge -128 \leq \langle\langle \mathbf{r1}_u^* \rangle\rangle \leq 127 \end{cases}$$

Now consider the overflow mode. Observe that the output value of $\langle\langle \mathbf{r0}_\ell^* \rangle\rangle$ can be found by solving a linear programming problem that minimises $\langle\langle \mathbf{r0}_\ell^* \rangle\rangle$ subject to the linear system $r \wedge \alpha_{\text{oct}}(\varphi_O(\mathbf{c}), S') \wedge \alpha_{\text{aff}}(f_{O,\text{simp}}, S) \wedge r'$. This gives $\langle\langle \mathbf{r0}_\ell^* \rangle\rangle = -128$. By solving three more linear programming problems we can likewise deduce $\langle\langle \mathbf{r0}_u^* \rangle\rangle = -125$, $\langle\langle \mathbf{r1}_\ell^* \rangle\rangle = 1$ and $\langle\langle \mathbf{r1}_u^* \rangle\rangle = 126$. When repeating this process for the underflow mode, we find that the system $r \wedge \alpha_{\text{oct}}(\varphi_U(\mathbf{c}), S') \wedge \alpha_{\text{aff}}(f_{U,\text{simp}}, S) \wedge r'$ is infeasible, hence this guarded update is not applicable for the given input range. However, the final mode is applicable (like the first) and gives the extrema: $\langle\langle \mathbf{r0}_\ell^* \rangle\rangle = -1$, $\langle\langle \mathbf{r0}_u^* \rangle\rangle = 127$, $\langle\langle \mathbf{r1}_\ell^* \rangle\rangle = 1$ and $\langle\langle \mathbf{r1}_u^* \rangle\rangle = 126$. More generally, evaluating a system of m guarded updates for a block that involves n variables will require at most $2mn$ linear programs to be solved.

The overall result is obtained by merging the results from different modes, and there is no reason why power sets could not be deployed to summarise the final value of $\langle\langle \mathbf{r0} \rangle\rangle$ as $[-128, -125] \cup [-1, 127]$ (with the understanding that adjacent and overlapping intervals are merged for compactness).

2.3 Deriving a transfer function for a block with many modes

Consider the following non-trivial sequence of instructions:

1: INC R0; 2: MOV R1, R0; 3: LSL R1;
4: SBC R1, R1; 5: EOR R0, R1; 6: SUB R0, R1;

INC R0 increments R0; MOV R1, R0 copies the contents of R0 into R1; LSL R1 leftshifts R1 by one bit position setting the carry to the sign; SBC R1, R1 subtracts R1, summed with the carry, from R1 and stores the result in R1; and SUB R0, R1 subtracts R1 from R0 without considering the carry. (The net effect of instructions 3 and 4 is to set all the lower bits of R1 to its sign bit, so that R1 contains either 0 or -1.)

Analogous to before, the sequence is bit-blasted using additional bit-vectors \mathbf{w} , \mathbf{x} , \mathbf{y} , and \mathbf{z} to represent intermediate values of registers: \mathbf{w} for the value of R0 immediately after instruction 1 (and the value of R1 after instruction 2); \mathbf{x} for the value of R0 after instruction 5; \mathbf{y} for the negation of R1 after instruction 5 which is then used in the following subtraction; and \mathbf{z} for the carry bits that are also used in subtract. With some simplification (needed to make the presentation accessible) the semantics of the block can be expressed as:

$$\varphi(\mathbf{w}, \mathbf{x}, \mathbf{y}, \mathbf{z}) = \left\{ \begin{array}{l} (\wedge_{i=0}^7 \mathbf{w}[i] \leftrightarrow (\mathbf{r0}[i] \oplus \wedge_{j=0}^{i-1} \mathbf{r0}[j])) \quad \wedge \\ (\wedge_{i=0}^7 \mathbf{r1}'[i] \leftrightarrow \mathbf{w}[7]) \quad \wedge \\ (\wedge_{i=0}^7 \mathbf{x}[i] \leftrightarrow (\mathbf{w}[i] \oplus \mathbf{r1}'[i])) \quad \wedge \\ (\wedge_{i=0}^7 \mathbf{y}[i] \leftrightarrow (\neg \mathbf{r1}'[i] \oplus \wedge_{j=0}^{i-1} \neg \mathbf{r1}'[j])) \quad \wedge \\ (\neg \mathbf{z}[0]) \quad \wedge \\ (\wedge_{i=0}^6 \mathbf{z}[i+1] \leftrightarrow (\mathbf{x}[i] \wedge \mathbf{y}[i]) \vee (\mathbf{x}[i] \wedge \mathbf{z}[i]) \vee (\mathbf{y}[i] \wedge \mathbf{z}[i])) \quad \wedge \\ (\wedge_{i=0}^7 \mathbf{r0}'[i] \leftrightarrow \mathbf{x}[i] \oplus \mathbf{y}[i] \oplus \mathbf{z}[i]) \end{array} \right.$$

For brevity, let vector \mathbf{u} denote the concatenation of the vectors \mathbf{w} , \mathbf{x} , \mathbf{y} and \mathbf{z} so as to write $\varphi(\mathbf{u})$ for $\varphi(\mathbf{w}, \mathbf{x}, \mathbf{y}, \mathbf{z})$.

For the increment, there are two modes of operation depending on whether it overflows or not. These can be expressed by: $\mu_O = (\neg \mathbf{r0}[7]) \wedge (\wedge_{i=0}^6 \mathbf{r0}[i])$ and $\mu_E = \neg \mu_O$ respectively. For the leftshift, there are two modes depending on whether it overflows or not, as defined by the formulae $\eta_O = \mathbf{w}[7]$ and $\eta_E = \neg \eta_O$ respectively. For subtraction, there are again three modes according to whether it overflows or underflows or does neither. These modes can be expressed as: $\nu_U = (\neg \mathbf{r0}'[7] \wedge \mathbf{x}[7] \wedge \mathbf{y}[7])$, $\nu_O = (\mathbf{r0}'[7] \wedge \neg \mathbf{x}[7] \wedge \neg \mathbf{y}[7])$ and $\nu_E = (\neg \nu_U) \wedge (\neg \nu_O)$. All other instructions in the block are unimodal; indeed they neither overflow nor underflow [1]. This gives twelve different mode combinations overall. However, the following formulae are unsatisfiable:

$$\begin{array}{lll} \mu_O \wedge \eta_O \wedge \nu_O \wedge \varphi(\mathbf{u}) & \mu_O \wedge \eta_O \wedge \nu_E \wedge \varphi(\mathbf{u}) & \mu_O \wedge \eta_E \wedge \nu_U \wedge \varphi(\mathbf{u}) \\ \mu_O \wedge \eta_E \wedge \nu_O \wedge \varphi(\mathbf{u}) & \mu_O \wedge \eta_E \wedge \nu_E \wedge \varphi(\mathbf{u}) & \mu_E \wedge \eta_O \wedge \nu_U \wedge \varphi(\mathbf{u}) \\ \mu_E \wedge \eta_O \wedge \nu_O \wedge \varphi(\mathbf{u}) & \mu_E \wedge \eta_E \wedge \nu_U \wedge \varphi(\mathbf{u}) & \mu_E \wedge \eta_E \wedge \nu_O \wedge \varphi(\mathbf{u}) \end{array}$$

indicating these mode combinations are not feasible. Henceforth, only three combinations needed to be considered when synthesising the action system.

Hence let $\varphi_1(\mathbf{u}) = \mu_O \wedge \eta_O \wedge \nu_U \wedge \varphi(\mathbf{u})$, $\varphi_2(\mathbf{u}) = \mu_E \wedge \eta_O \wedge \nu_E \wedge \varphi(\mathbf{u})$ and $\varphi_3(\mathbf{u}) = \mu_E \wedge \eta_E \wedge \nu_E \wedge \varphi(\mathbf{u})$. The inputs to the block are $S' = \{\mathbf{r0}, \mathbf{r1}\}$ and calculating guards for these combinations gives:

$$\begin{aligned}\alpha_{\text{oct}}(\varphi_1(\mathbf{v}), S') &= 127 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq 127 \wedge -128 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 127 \\ \alpha_{\text{oct}}(\varphi_2(\mathbf{v}), S') &= -128 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq -2 \wedge -128 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 127 \\ \alpha_{\text{oct}}(\varphi_3(\mathbf{v}), S') &= -1 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq 126 \wedge -128 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 127\end{aligned}$$

Note that all three guards impose vacuous constraints on $\langle\langle \mathbf{r1} \rangle\rangle$. This is because R1 is written before it is read (which could be inferred prior to deriving the transfer functions though this is not strictly necessary).

As before, the updates are computed for each $\varphi_i(\mathbf{u})$ from three formulae $f_{i,\text{simp}} = \theta_i(\mathbf{u}, \mathbf{u}') \wedge \psi_i(\mathbf{v}, \mathbf{v}')$ where $\theta_i(\mathbf{u}, \mathbf{u}')$ and $\psi_i(\mathbf{v}, \mathbf{v}')$ are quantifier-free and derived from $\varphi_i(\mathbf{u})$ as previously. Hence we calculate affine relationships over $S = \{\mathbf{r0}_\ell^*, \mathbf{r0}_u^*, \mathbf{r1}_\ell^*, \mathbf{r1}_u^*, \mathbf{r0}_\ell, \mathbf{r0}_u, \mathbf{r1}_\ell, \mathbf{r1}_u\}$ which yields:

$$\begin{aligned}\alpha_{\text{aff}}(f_{1,\text{simp}}, S) &= \langle\langle \mathbf{r0} \rangle\rangle_\ell = 127 && \wedge \langle\langle \mathbf{r0} \rangle\rangle_u = 127 && \wedge \\ & \langle\langle \mathbf{r0} \rangle\rangle_\ell^* = -128 && \wedge \langle\langle \mathbf{r0} \rangle\rangle_u^* = -128 \\ \alpha_{\text{aff}}(f_{2,\text{simp}}, S) &= \langle\langle \mathbf{r0} \rangle\rangle_\ell^* = -\langle\langle \mathbf{r0} \rangle\rangle_u - 1 && \wedge \langle\langle \mathbf{r0} \rangle\rangle_u^* = -\langle\langle \mathbf{r0} \rangle\rangle_\ell - 1 \\ \alpha_{\text{aff}}(f_{3,\text{simp}}, S) &= \langle\langle \mathbf{r0} \rangle\rangle_\ell^* = \langle\langle \mathbf{r0} \rangle\rangle_\ell + 1 && \wedge \langle\langle \mathbf{r0} \rangle\rangle_u^* = \langle\langle \mathbf{r0} \rangle\rangle_u + 1\end{aligned}$$

Note that no affine constraints are inferred for $\mathbf{r1}_\ell^*$ and $\mathbf{r1}_u^*$ reflecting that R1 is used merely to store an intermediate value (though combining affine equations with congruences [14] would preserve some information pertaining to the final value of R1). Note how ranges are swapped for the second mode since in this circumstance R0 is negative. From the resulting action system, it can be seen that the block overwrites R0 with the absolute value of $(\mathbf{R0} + 1)$ subject to wrap around ($128 = -128 \pmod{256}$). To the best of our knowledge, no other approach can derive a useful transfer function for a block such as this.

3 Abstracting Boolean Formulae

This section shows how to construct octagonal and affine abstractions of a given Boolean formula $\varphi(\mathbf{v})$ defined over a set of bit-vectors $S = \{\mathbf{x}_1, \dots, \mathbf{x}_n\}$ of size k and a single bit-vector \mathbf{v} that identifies any intermediate variables. For presentational purposes, we focus on deriving abstractions that relate the values of $\langle\langle \mathbf{x}_1 \rangle\rangle, \dots, \langle\langle \mathbf{x}_n \rangle\rangle$, though the construction for unsigned values is analogous.

3.1 Abstracting Boolean Formulae with Octagonal Inequalities

Let $\lambda, \mu \in \{-1, 0, 1\}$ and $1 \leq i \leq j \leq n$ be fixed and consider the derivation of an octagonal inequality $\lambda \langle\langle \mathbf{x}_i \rangle\rangle + \mu \langle\langle \mathbf{x}_j \rangle\rangle \leq c^*$ where $c^* \in \mathbb{Z}$. Since k is fixed it follows $-2^k \leq \lambda \langle\langle \mathbf{x}_i \rangle\rangle + \mu \langle\langle \mathbf{x}_j \rangle\rangle \leq 2^k$, hence the problem reduces to finding the least $-2^k \leq c^* \leq 2^k$ such that if $\varphi(\mathbf{v})$ holds then $\lambda \langle\langle \mathbf{x}_i \rangle\rangle + \mu \langle\langle \mathbf{x}_j \rangle\rangle \leq c^*$ also holds. To this end, let \mathbf{c}^* denote a bit-vector of size $k + 2$ and suppose $\phi_{\lambda, \mu, \mathbf{x}_i, \mathbf{x}_j}(\mathbf{u})$ is a propositional encoding of $\lambda \langle\langle \mathbf{x}_i \rangle\rangle + \mu \langle\langle \mathbf{x}_j \rangle\rangle \leq \mathbf{c}^*$ where the sum is calculated

to a width of $k + 2$ bits and \leq is encoded as in Sect. 2.1, likewise operating on vectors of size $k + 2$. In this formulation, the vector \mathbf{v} denotes the intermediate variables required for addition and negation. Since \mathbf{x}_i and \mathbf{x}_j are k -bit this construction avoids wraps, hence $\phi_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{v})$ holds iff $\lambda\langle\langle\mathbf{x}_i\rangle\rangle + \mu\langle\langle\mathbf{x}_j\rangle\rangle \leq c^*$ holds. Likewise, suppose that $\phi'_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{v}')$ is a propositional formula that holds iff $\lambda\langle\langle\mathbf{x}_i\rangle\rangle + \mu\langle\langle\mathbf{x}_j\rangle\rangle \leq c'$ holds where \mathbf{c}' is $k+2$ bit-vector distinct from \mathbf{c}^* . Finally, let κ denote a Boolean formula that holds iff $\mathbf{c}^* \leq \mathbf{c}'$ holds.

Single inequalities With the formulae $\phi_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{v})$ and $\phi'_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{v}')$ thus defined, we can apply universal quantification to specify the least c^* as the unique value $\langle\langle\mathbf{c}^*\rangle\rangle$ which satisfies $\theta_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{u}, \mathbf{v}) \wedge \psi_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{u}', \mathbf{v}')$ where:

$$\begin{aligned} \theta_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{u}, \mathbf{v}) &= \forall \mathbf{x}_i : \forall \mathbf{x}_j : (\varphi(\mathbf{u}) \Rightarrow \phi_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{v})) \\ \psi_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{u}', \mathbf{v}') &= \forall \mathbf{x}_i : \forall \mathbf{x}_j : \forall \mathbf{c}' : ((\varphi(\mathbf{u}') \Rightarrow \phi'_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{v}')) \Rightarrow \kappa) \end{aligned}$$

and \mathbf{u} and \mathbf{u}' are renamed apart so as to avoid cross-coupling. More generally, the octagonal abstraction of $\varphi(\mathbf{v})$ over the set S is given by:

$$\alpha_{\text{oct}}(\varphi(\mathbf{v}), S) = \bigwedge \left\{ \begin{array}{l} \lambda\langle\langle\mathbf{x}_i\rangle\rangle + \mu\langle\langle\mathbf{x}_j\rangle\rangle \leq \langle\langle\mathbf{c}^*\rangle\rangle \\ \exists \lambda, \mu \in \{-1, 0, 1\} \\ \exists 1 \leq i \leq j \leq n \\ \theta_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{u}, \mathbf{v}) \wedge \psi_{\lambda,\mu,\mathbf{x}_i,\mathbf{x}_j}(\mathbf{u}', \mathbf{v}') \text{ holds} \end{array} \right\}$$

Note that in $\lambda\langle\langle\mathbf{x}_i\rangle\rangle + \mu\langle\langle\mathbf{x}_j\rangle\rangle \leq \langle\langle\mathbf{c}^*\rangle\rangle$ the symbols $\langle\langle\mathbf{x}_i\rangle\rangle$ and $\langle\langle\mathbf{x}_j\rangle\rangle$ denote variables whereas $\langle\langle\mathbf{c}^*\rangle\rangle$ is a value that is fixed by the formula $\theta_{\lambda,\mu,i,j}(\mathbf{u}, \mathbf{v}) \wedge \psi_{\lambda,\mu,i,j}(\mathbf{u}', \mathbf{v}')$. Of course, as previously explained, quantifier-free versions of $\theta_{\lambda,\mu,i,j}(\mathbf{u}, \mathbf{v})$ and $\psi_{\lambda,\mu,i,j}(\mathbf{u}', \mathbf{v}')$ can be obtained through CNF conversion and clause simplification, hence $\alpha_{\text{oct}}(\varphi(\mathbf{v}), S)$ can be computed as well as specified. The resulting octagonal abstraction is closed (though this is not necessary in our setting).

Many inequalities Interestingly, many inequalities can be derived in a single call to the solver. Let $\{(\mathbf{y}_1, \mathbf{z}_1), \dots, (\mathbf{y}_m, \mathbf{z}_m)\} \subseteq S^2$ and $\{(\lambda_1, \mu_1), \dots, (\lambda_m, \mu_m)\} \subseteq \{-1, 0, 1\}^2$, and consider the problem of finding the set $\{c_1^*, \dots, c_m^*\} \subseteq [-2^k, 2^k - 1]$ of least values such that if $\varphi(\mathbf{v})$ holds then $\lambda_i\langle\langle\mathbf{y}_i\rangle\rangle + \mu_i\langle\langle\mathbf{z}_i\rangle\rangle \leq c_i^*$ holds. This problem can be formulated in an analogous way to before using bit-vectors $\mathbf{c}_1^*, \dots, \mathbf{c}_m^*$ and $\mathbf{c}'_1, \dots, \mathbf{c}'_m$ all of size $k + 2$. Furthermore, let κ_i be a propositional formula that holds iff $\mathbf{c}_i^* \leq \mathbf{c}'_i$ holds. Then the problem of simultaneously finding all the c_i^* amounts to solving $\theta(\mathbf{u}, \mathbf{v}_1, \dots, \mathbf{v}_m) \wedge \psi(\mathbf{u}', \mathbf{v}'_1, \dots, \mathbf{v}'_m)$ where:

$$\begin{aligned} \theta(\mathbf{u}, \mathbf{v}_1, \dots, \mathbf{v}_m) &= \forall \mathbf{y}_1 : \forall \mathbf{z}_1 : \dots : \forall \mathbf{y}_k : \forall \mathbf{z}_k : (\varphi(\mathbf{u}) \Rightarrow \bigwedge_{k=1}^m \phi_{\lambda_k, \mu_k, \mathbf{y}_k, \mathbf{z}_k}(\mathbf{v}_k)) \\ \psi(\mathbf{u}, \mathbf{v}'_1, \dots, \mathbf{v}'_m) &= \forall \mathbf{y}_1 : \forall \mathbf{z}_1 : \dots : \forall \mathbf{y}_k : \forall \mathbf{z}_k : \\ &\quad \forall \mathbf{c}'_1 : \dots : \forall \mathbf{c}'_m : ((\varphi(\mathbf{u}') \Rightarrow \bigwedge_{k=1}^m \phi'_{\lambda_k, \mu_k, \mathbf{y}_k, \mathbf{z}_k}(\mathbf{v}'_k)) \Rightarrow \bigwedge_{k=1}^m \kappa_k) \end{aligned}$$

Notice that $\theta(\mathbf{u}, \mathbf{v}_1, \dots, \mathbf{v}_m) \wedge \psi(\mathbf{u}', \mathbf{v}'_1, \dots, \mathbf{v}'_m)$ involves one copy of $\varphi(\mathbf{u})$ and another of $\varphi(\mathbf{u}')$ rather than m copies of both.

Timings The time required to bit-blast the blocks in Sect. 2 and then eliminate the universal quantifiers were essentially non-measurable. The time required to prove unsatisfiability or compute the guards for the various mode combinations varied between 0.1s and 0.6s. Octagons were derived one inequality at a time (rather than several together) using the SAT4J solver [16] on 2.6GHz MacBook Pro. Incremental SAT solving would speedup the generation of octagons, as the intermediate results used to derive one inequality could be used to infer another.

3.2 Abstracting Boolean Formulae with Affine Equalities

Affine equations [13, 22] are related to congruences [11, 23]; indeed the former is a special case of the latter where the modulo is 0. This suggests adapting an abstraction technique for formulae that discovers congruence relationships between the propositional variables of a given formula [14]. In our setting, the problem is different. It is that of computing an affine abstraction of a formula $\varphi(\mathbf{v})$ defined over a set of bit-vectors S . As before, we do not aspire to derive relationships that involve intermediate variables \mathbf{v} and we assume each \mathbf{x}_i is signed.

Algorithm Figure 1 gives an algorithm for computing $\alpha_{\text{aff}}(\varphi(\mathbf{v}), S)$. In what follows, the n -ary vector \mathbf{x} is defined as $\mathbf{x} = (\langle\langle\mathbf{x}_1\rangle\rangle, \dots, \langle\langle\mathbf{x}_n\rangle\rangle)$. Affine equations over S are represented with an augmented rational matrix $[A \mid \mathbf{b}]$ that we interpret as defining the set $\{\mathbf{x} \in [-2^{k-1}, 2^{k-1}]^n \mid A\mathbf{x} = \mathbf{b}\}$. The algorithm relies on a propositional encoding for an affine disequality constraint $(c_1, \dots, c_n) \cdot \mathbf{x} \neq b$ where $c_1, \dots, c_n, b \in \mathbb{Q}$. To see that such an encoding is possible assume, without loss of generality, that the disequality is integral and b is non-negative. Then rewrite the disequality as $(c_1^+, \dots, c_n^+) \cdot \mathbf{x} \neq b + (c_1^-, \dots, c_n^-) \cdot \mathbf{x}$ where $(c_1^+, \dots, c_n^+), (c_1^-, \dots, c_n^-) \in \mathbb{N}^n$ and $\mathbb{N} = \{i \in \mathbb{Z} \mid 0 \leq i\}$. Let $c^+ = \sum_{i=1}^n c_i^+$ and $c^- = \sum_{i=1}^n c_i^-$. Since each $\langle\langle\mathbf{x}_j\rangle\rangle \in [-2^{k-1}, 2^{k-1} - 1]$ it follows that computing the sums $(c_1^+, \dots, c_n^+) \cdot \mathbf{x}$ and $b + (c_1^-, \dots, c_n^-) \cdot \mathbf{x}$ with a signed $1 + \lceil \log_2(1 + \max(2^k c^+, b + 2^k c^-)) \rceil$ bit representation is sufficient to avoid wraps, allowing the disequality to be modelled exactly as a formula. In the algorithm, this formula is denoted $\phi(\mathbf{w})$ where \mathbf{w} is a vector of temporary variables used for carry bits and intermediate sums.

Apart from $\phi(\mathbf{w})$, the abstraction algorithm is essentially the same as that proposed for congruences [14] (with a proof of correctness carrying over too). The algorithm starts with an unsatisfiable constraint $\mathbf{0} \cdot \mathbf{x} = 1$ which is successively relaxed by merging it with a series of affine systems that are derived by SAT (or SMT) solving. The truth assignment θ is considered to be a mapping $\theta : \text{var}(\varphi(\mathbf{v}) \wedge \phi(\mathbf{w})) \rightarrow \{0, 1\}$ which, when applied to a k -bit vector of variables such as \mathbf{x}_j , yields a binary vector. Such a binary vector can then be interpreted as a signed number to give a value in the range $[-2^{k-1}, 2^{k-1} - 1]$. This construction is applied in lines 5-8 to find a vector $(\langle\langle\theta(\mathbf{x}_1)\rangle\rangle, \dots, \langle\langle\theta(\mathbf{x}_n)\rangle\rangle) \in [-2^{k-1}, 2^{k-1} - 1]^n$ which satisfies both the disequality $(a_1, \dots, a_n) \cdot \mathbf{x} \neq b$ and the formula $\varphi(\mathbf{v})$.

The algorithm is formulated in terms of some auxiliary functions: $\text{row}(M, i)$ extracts row i from the matrix M where the first row is taken to be row 1 (rather

```

(1) function affine( $\varphi(\mathbf{v}), \{\mathbf{x}_1, \dots, \mathbf{x}_n\}$ )
(2)    $[A \mid \mathbf{b}] := [0, \dots, 0 \mid 1]$ ;
(3)    $i := 0; r := 1$ ;
(4)   while  $i < r$  do
(5)      $(a_1, \dots, a_n, b) := \text{row}([A \mid \mathbf{b}], r - i)$ ;
(6)     let  $\phi(\mathbf{w})$  hold iff  $(a_1, \dots, a_n) \cdot \mathbf{x} \neq b$  holds;
(7)     if  $\varphi(\mathbf{v}) \wedge \phi(\mathbf{w})$  has a satisfying truth assignment  $\theta$ 
(8)        $[A' \mid \mathbf{b}'] := [A \mid \mathbf{b}] \sqcup_{\text{aff}} [\text{Id} \mid (\langle\langle\theta(\mathbf{x}_1)\rangle\rangle, \dots, \langle\langle\theta(\mathbf{x}_n)\rangle\rangle)^T]$ ;
(9)        $[A \mid \mathbf{b}] := \text{triangular}([A' \mid \mathbf{b}'])$ ;
(10)       $r := \text{number\_of\_rows}([A \mid \mathbf{b}])$ ;
(11)     else  $i := i + 1$ ;
(12)   endwhile
(13) return  $[A \mid \mathbf{b}]$ 

```

Fig. 1. Calculating the affine closure of the Boolean formula $\varphi(\mathbf{v})$

than 0); $\text{triangular}(M)$ puts M into an upper triangular form using Gaussian elimination; and $\text{number_of_rows}(M)$ returns the number of rows in M .

The rows of $[A \mid \mathbf{b}]$ are considered in reverse order. Each iteration of the loop tests whether there exists a truth assignment of $\varphi(\mathbf{v})$ that also satisfies the $\phi(\mathbf{w})$ formula constructed from row $r - i$. If not, then every model of $\varphi(\mathbf{v})$ satisfies the affine equality $(a_1, \dots, a_n) \cdot \mathbf{x} = b$ represented by row $r - i$. Hence the equality constitutes a description of the formula. The counter i is then incremented to examine a row which, thus far, has not been considered. Conversely, if the instance is satisfiable, then the solution is represented as a matrix $[\text{Id} \mid (\langle\langle\theta(\mathbf{x}_1)\rangle\rangle, \dots, \langle\langle\theta(\mathbf{x}_n)\rangle\rangle)^T]$ which is merged with $[A \mid \mathbf{b}]$. Merge is an $O(n^3)$ operation [13] that yields a new summary $[A' \mid \mathbf{b}']$ that enlarges $[A \mid \mathbf{b}]$ with the freshly found solution. The next iteration of the loop will either relax $[A \mid \mathbf{b}]$ by finding another solution, or verify that the row now describes $\varphi(\mathbf{v})$. Triangular form ensures that all rows beneath the one under consideration will never be effected by the merge. At most n iterations are required since the affine systems enumerated by the algorithm constitute an ascending chain over n variables [13].

Example Consider $\varphi(\mathbf{w}, \mathbf{x})$ which is an encoding of $\langle\langle \mathbf{z} \rangle\rangle = 2(\langle\langle \mathbf{v} \rangle\rangle + 1) + \langle\langle \mathbf{y} \rangle\rangle$ subject to the additional constraints that $-32 \leq \langle\langle \mathbf{v} \rangle\rangle \leq 31$ and $-32 \leq \langle\langle \mathbf{y} \rangle\rangle \leq 31$:

$$\varphi(\mathbf{w}, \mathbf{x}) = \begin{cases} (\neg \mathbf{w}[0]) \wedge (\wedge_{i=0}^6 \mathbf{w}[i+1] \leftrightarrow (\mathbf{v}[i] \oplus \wedge_{j=0}^{i-1} \mathbf{v}[j])) & \wedge \\ (\neg \mathbf{x}[0]) & \wedge \\ (\wedge_{i=0}^6 \mathbf{x}[i+1] \leftrightarrow (\mathbf{w}[i] \wedge \mathbf{x}[i]) \vee (\mathbf{w}[i] \wedge \mathbf{y}[i]) \vee (\mathbf{x}[i] \wedge \mathbf{y}[i])) & \wedge \\ (\wedge_{i=0}^7 \mathbf{z}[i] \leftrightarrow \mathbf{w}[i] \oplus \mathbf{x}[i] \oplus \mathbf{y}[i]) & \wedge \\ ((\mathbf{v}[7] \leftrightarrow \mathbf{v}[6]) \wedge (\mathbf{v}[6] \leftrightarrow \mathbf{v}[5])) \wedge ((\mathbf{y}[7] \leftrightarrow \mathbf{y}[6]) \wedge (\mathbf{y}[6] \leftrightarrow \mathbf{y}[5])) & \end{cases}$$

Suppose $\mathbf{x}_1 = \mathbf{v}$, $\mathbf{x}_2 = \mathbf{y}$ and $\mathbf{x}_3 = \mathbf{z}$. The solutions that are found in each iteration are given in the left hand column. The $[A \mid \mathbf{b}]$ and $[A' \mid \mathbf{b}']$ are immediately left and right of the equality. The arrow indicates the row under consideration. The unsatisfiable case is first encountered in the final iteration. The algorithm returns $[2, 1, -1 \mid -2]$ and thus recovers $2\langle\langle \mathbf{v} \rangle\rangle + \langle\langle \mathbf{y} \rangle\rangle - \langle\langle \mathbf{z} \rangle\rangle = -2$ from $\varphi(\mathbf{v})$.

$$\begin{aligned}
(0, 0, 2) \quad [0 \ 0 \ 0 \mid 1] &\sqcup_{\text{aff}} \begin{bmatrix} 1 \ 0 \ 0 \mid 0 \\ 0 \ 1 \ 0 \mid 0 \\ 0 \ 0 \ 1 \mid 2 \end{bmatrix} = \begin{bmatrix} 1 \ 0 \ 0 \mid 0 \\ 0 \ 1 \ 0 \mid 0 \\ 0 \ 0 \ 1 \mid 2 \leftarrow \end{bmatrix} \\
(-1, 0, 0) \quad \begin{bmatrix} 1 \ 0 \ 0 \mid 0 \\ 0 \ 1 \ 0 \mid 0 \\ 0 \ 0 \ 1 \mid 2 \end{bmatrix} &\sqcup_{\text{aff}} \begin{bmatrix} 1 \ 0 \ 0 \mid -1 \\ 0 \ 1 \ 0 \mid 0 \\ 0 \ 0 \ 1 \mid 0 \end{bmatrix} = \begin{bmatrix} 2 \ 0 \ -1 \mid -2 \\ 0 \ 1 \ 0 \mid 0 \leftarrow \end{bmatrix} \\
(0, 1, 3) \quad \begin{bmatrix} 2 \ 0 \ -1 \mid -2 \\ 0 \ 1 \ 0 \mid 0 \end{bmatrix} &\sqcup_{\text{aff}} \begin{bmatrix} 1 \ 0 \ 0 \mid 0 \\ 0 \ 1 \ 0 \mid 1 \\ 0 \ 0 \ 1 \mid 3 \end{bmatrix} = [2 \ 1 \ -1 \mid -2 \leftarrow]
\end{aligned}$$

Timings Computing affine abstractions for the feasible mode combinations in the examples of the previous section took no longer than 0.4s per mode. Each mode required no more than 5 SAT instances to be solved (which is considerably fewer than that required for inferring bit-level congruences [14]) with the join taking less than 5% of the overall runtime.

4 Applying Action Systems of Guarded Updates

Thus far we have derived transfer functions that are action systems of guarded updates $T = \{(g_1, u_1), \dots, (g_m, u_m)\}$ where each guard g_i is a system of octagonal constraints over a set of bit-vectors $S' = \{\mathbf{x}_i \mid i \in [1, n]\}$ and each update u_i is a system of affine constraints over $S = \{\mathbf{x}_{i,\ell}, \mathbf{x}_{i,u}, \mathbf{x}'_{i,\ell}, \mathbf{x}'_{i,u} \mid i \in [1, n]\}$. In this section we show that the application of such an action system can be reduced to linear programming. To do so, we continue working with the assumption that S and S' are all k -bit and represent signed objects.

Thus consider applying T to a system of interval constraints over S' of the form $c = \bigwedge_{i=1}^n \ell_i \leq \langle \mathbf{x}_i \rangle \leq u_i$ where $\{\ell_1, u_1, \dots, \ell_n, u_n\} \subseteq [-2^{k-1}, 2^{k-1} - 1]$. In particular, consider the application of the guarded update (g_k, u_k) . Suppose $g_k = \bigwedge_{i=1}^p \lambda_i \cdot \mathbf{x} \leq d_i$ where \mathbf{x} is the n -ary vector $\mathbf{x} = (\langle \mathbf{x}_1 \rangle, \dots, \langle \mathbf{x}_n \rangle)$ and each n -ary coefficient vector $\lambda_i \in \{-1, 0, 1\}^n$ has no more than two non-zero elements and $d_i \in \mathbb{Z}$. Furthermore, denote

$$\begin{aligned}
\mathbf{x}_\ell &= (\langle \mathbf{x}_{1,\ell} \rangle, \dots, \langle \mathbf{x}_{n,\ell} \rangle) & \mathbf{x}_u &= (\langle \mathbf{x}_{1,u} \rangle, \dots, \langle \mathbf{x}_{n,u} \rangle) \\
\mathbf{x}'_\ell &= (\langle \mathbf{x}'_{1,\ell} \rangle, \dots, \langle \mathbf{x}'_{n,\ell} \rangle) & \mathbf{x}'_u &= (\langle \mathbf{x}'_{1,u} \rangle, \dots, \langle \mathbf{x}'_{n,u} \rangle)
\end{aligned}$$

Then u_k can be written as $u_k = \bigwedge_{i=1}^q \mu_{i,\ell} \cdot \mathbf{x}_\ell + \mu_{i,u} \cdot \mathbf{x}_u + \mu'_{i,\ell} \cdot \mathbf{x}'_\ell + \mu'_{i,u} \cdot \mathbf{x}'_u = f_i$ where each n -ary vector $\mu_{i,\ell}, \mu_{i,u}, \mu'_{i,\ell}, \mu'_{i,u} \in \mathbb{Z}^n$ and $f_i \in \mathbb{Z}$. To specify a linear program, let \mathbf{e}_j denote the n -ary elementary vector $(0, \dots, 0, 1, 0, \dots, 0)$ where $j - 1$ zeros precede the one and $n - j$ zeros follow it. Then the value of $\langle \mathbf{x}'_{j,\ell} \rangle$ for any $j \in [1, n]$ can be found by minimising $\mathbf{e}_j \cdot \mathbf{x}'_\ell$ subject to:

$$P = \begin{cases} \bigwedge_{i=1}^q \mu_{i,\ell} \cdot \mathbf{x}_\ell + \mu_{i,u} \cdot \mathbf{x}_u + \mu'_{i,\ell} \cdot \mathbf{x}'_\ell + \mu'_{i,u} \cdot \mathbf{x}'_u = f_i & \wedge \\ \bigwedge_{i=1}^n \mathbf{e}_i \cdot \mathbf{x}_\ell = \ell_i & \wedge \quad \bigwedge_{i=1}^n \mathbf{e}_i \cdot \mathbf{x}_u = u_i & \wedge \\ \bigwedge_{i=1}^n \mathbf{e}_i \cdot \mathbf{x} - \mathbf{e}_i \cdot \mathbf{x}_u \leq 0 & \wedge \quad \bigwedge_{i=1}^n \mathbf{e}_i \cdot \mathbf{x}_\ell - \mathbf{e}_i \cdot \mathbf{x} \leq 0 & \wedge \\ \bigwedge_{i=1}^n \mathbf{e}_i \cdot \mathbf{x}'_u \leq 2^{k-1} - 1 & \wedge \quad \bigwedge_{i=1}^n -\mathbf{e}_i \cdot \mathbf{x}'_\ell \leq 2^{k-1} & \wedge \\ \bigwedge_{i=1}^p \lambda_i \cdot \mathbf{x} \leq d_i & & \end{cases}$$

We let ℓ'_j denote this minima. Conversely let u'_j denote the value found by maximising $e_j \cdot \mathbf{x}'_u$ subject to P . Note that although P is bounded, it is not necessarily feasible, which will be detected when solving the linear program (the first stage of two-phase simplex amounts of deciding feasibility by solving the so-called auxiliary problem [4]).

If P is feasible, the system of intervals generated for (g_k, u_k) is given by $\bigwedge_{j=1}^n \ell'_j \leq \langle\langle \mathbf{x}_j \rangle\rangle \leq u'_j$. If infeasible, the unsatisfiable constraint \perp is output. Merging the systems generated by all the guarded updates (using power sets of intervals if desired) then gives the final output system of interval constraints.

Rationale The rationale for evaluating transfer functions with linear programming is the same as that which motivated deriving transfer functions with SAT: efficient solvers are readily (even freely) available for both. Moreover, although linear solvers have suffered problems relating to floats, steady progress has been made on both speeding up exact solvers [9] and deriving safe bounds on optima [24]. Thus soundness is no longer an insurmountable problem. We do not offer timings for evaluating transfer functions, partly because the focus of this paper (reflecting that of others [14, 20, 26]) is on deriving them; and partly because the linear programs that arise from the examples are trivial by industrial standards.

Linear programming versus closure Since the guards are octagonal one might wonder whether linear programming could be replaced with a closure calculation on octagons [19] that combines the interval constraints (which constitute a degenerate form of octagon) with the guard, thereby refining the interval bounds. These improved bounds could then be used to calculate the updates, without using linear programming. To demonstrate why this approach is sub-optimal, let $c = (0 \leq \langle\langle \mathbf{r0} \rangle\rangle \leq 1) \wedge (0 \leq \langle\langle \mathbf{r1} \rangle\rangle \leq 1)$ and suppose the guard is the single constraint $g = \langle\langle \mathbf{r0} \rangle\rangle + \langle\langle \mathbf{r1} \rangle\rangle \leq 1$. The closure of $c \wedge g$ would not refine the upper bounds on $\langle\langle \mathbf{r0} \rangle\rangle$ and $\langle\langle \mathbf{r1} \rangle\rangle$. Thus if these values were substituted into the update $\langle\langle \mathbf{r0}^*_u \rangle\rangle = \langle\langle \mathbf{r0}_u \rangle\rangle + \langle\langle \mathbf{r1}_u \rangle\rangle$ then a maximal value of 2 would be derived for $\langle\langle \mathbf{r0}^*_u \rangle\rangle$. This is safe but observe that maximising $\langle\langle \mathbf{r0}^*_u \rangle\rangle$ subject to $c \wedge g$ yields the improved bound of 1, which illustrates why linear programming is preferable.

5 Related Work

The problem of computing transfer functions for numeric domains is as old as the field itself, and the seminal paper on polyhedral analysis discusses different ways to realise a transfer function for $x := x \times y$ [8, Sect. 4.2.1]. Granger [10] lamented the difficulty of handcrafting best transformers for congruences, but it took more than a decade before it was noticed that they can always be constructed for domains that satisfy the ascending chain condition [27]. The idea is to reformulate each application of a best transformer as a series of calls to a decision procedure such as a theorem prover. This differs from our work which aspires to evaluate a transfer function without a complicated decision procedure.

Contemporaneously it was observed that best transformers can be computed for intervals using BDDs [26]. The authors observe that if $g : [0, 2^8 - 1] \rightarrow$

$[0, 2^8 - 1]$ is a unary operation on an unsigned byte, then its best transformer $f : D \rightarrow D$ on $D = \{\emptyset\} \cup \{[\ell, u] \mid 0 \leq \ell \leq u < 2^8\}$ can be defined through interval subdivision. If $\ell = u$ then $f([\ell, u]) = g(\ell)$ whereas if $\ell < u$ then $f([\ell, u]) = f([\ell, m-1]) \sqcup f([m, u])$ where $m = \lfloor u/2^n \rfloor 2^n$ and $n = \lfloor \log_2(u-\ell+1) \rfloor$. Binary operations can likewise be decomposed. The 8-bit inputs, ℓ and u , can be represented as 8-bit vectors, as can the 8-bit outputs, so as to represent f with a BDD. This permits caching to be applied when f is computed, which reduces the time needed to compute a best transformer to approximately one day for each 8-bit operation. The approach does not scale to wider words nor to blocks.

Our work builds on that of Monniaux [20] who showed how transfer functions can be derived for operations over real-valued variables. His approach relies on universal quantifier elimination algorithm which is problematic for piecewise linear functions. Universal quantifier elimination also arises in work on inferring template constraints [12]. There the authors employ Farkas' lemma to transform universal quantification to existential quantification, albeit at the cost of compromising completeness (Farkas' lemma prevents integral reasoning).

The problem of handling limited precision arithmetic is discussed in [29]. Existing approaches are to: verify that no overflows arise using perfect numbers; revise the concretisation map to reflect truncation [29]; or deploy congruences [14, 23] where the modulo is a power of two. Our work suggests handling wraps in the generation of the transfer functions, which we think is natural.

6 Concluding Discussion

This paper advocates deriving transfer functions from Boolean formulae since the elimination of universal quantifiers is trivial in this domain. Boolean formulae are natural candidates for expressing arithmetic, logical and bitwise operations, allowing transfer functions to be derived for blocks of code that would otherwise only be amenable to the coarsest of approximation. The paper shows how to distill transfer functions that are action systems of guarded updates, where the guards are octagonal inequalities and the updates are linear affine equalities. This formulation enables the application of a transfer function for a basic block to be reduced to a series of linear programming problems. Although we have illustrated the approach using octagons, there is no reason why richer classes of template constraints [28] could not be deployed to express the guards. Moreover, linear affine equalities could be substituted with polynomial equalities of degree at most d [22], say, and correspondingly linear programming replaced with non-linear programming. Thus the ideas presented in the paper generalise quite naturally. Finally, the approach will only become more attractive as linear solvers and SAT solvers continue to improve both in terms of efficiency and scalability.

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